

In the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) An apparatus comprising:
a light emitting diode comprising a chip having a wavelength converting layer that forms a light emitting surface that emits light having a range of wavelengths into a medium with a refractive index of less than 1.25; and
a collimating optical element disposed to receive the light having only the range of wavelengths emitted from the light emitting surface of the chip, the collimating optical element having an entrance surface, wherein the medium is disposed between the entrance surface and the light emitting surface of the chip.
2. (Previously Presented) An apparatus comprising:
a light emitting diode comprising a chip having a light emitting surface that emits light into a medium with a refractive index of less than or equal to approximately 1.25; and
a collimating optical element disposed to receive the light emitted from the light emitting surface of the chip, the collimating optical element having an entrance surface, wherein the medium is disposed between the entrance surface and the light emitting surface of the chip, wherein the collimating optical element and the chip are separated by a distance that is less than or equal to approximately 50% of the width of the chip.
3. (Original) The apparatus of Claim 1, wherein the collimating optical element is a lens.
4. (Original) The apparatus of Claim 1, further comprising a holding element that holds the collimating optical element.
5. (Original) The apparatus of Claim 4, wherein the holding element has a ring shape and includes a notch and the lens has a tab that is held in the notch.
6. (Original) The apparatus of Claim 4, wherein the light emitting diode further comprises a submount, the chip being mounted on the submount, and wherein the holding element is mounted on the submount by reflow soldering.

7. (Original) The apparatus of Claim 1, further comprising a secondary collimating optical element disposed over the collimating optical element such that the collimating optical element is disposed between the second collimating optical element and the chip.
8. (Original) The apparatus of Claim 1, further comprising:
an array of light emitting diodes, each light emitting diode comprising a chip having a light emitting surface that emits light into a medium having a refractive index of less than or equal to approximately 1.25; and
at least one collimating optical element being disposed to receive the light emitted from the light emitting surfaces of each chip, the at least one collimating optical element having an entrance surface, wherein the medium is disposed between the entrance surface of the at least one collimating optical element and the light emitting surface of each chip.
9. (Original) The apparatus of Claim 8, wherein the at least one collimating optical element comprises an array of collimating optical elements, each collimating optical element being disposed to receive the light emitted from the light emitting surface of an associated chip, each collimating optical element having an entrance surface, wherein the medium is disposed between the entrance surface and the light emitting surface of the associated chip.
10. (Original) The apparatus of Claim 9, wherein the array of collimating optical elements is an integral array of lenses.
11. (Original) The apparatus of Claim 9, wherein at least one chip is displaced laterally with respect to the center of the associated collimating optical element.
12. (Original) The apparatus of Claim 1, wherein the light emitting diode further comprises a submount and an array of chips mounted on the submount, each chip in the array of chips having a light emitting surface that emits light into a medium having a refractive index of less than or equal to approximately 1.25, and wherein the collimating optical element is disposed to receive the light emitted from the light emitting surface of each chip in the array of chips.

13. (Original) The apparatus of Claim 1, further comprising a micro-display disposed to receive light emitted from the light emitting surface of the chip after passing through the collimating optical element.
14. (Original) The apparatus of Claim 1, wherein the chip includes one of a wavelength converting layer, a diffractive layer, a micro-refractive layer, and a filter layer and a polarizer layer that forms the light emitting surface.
15. (Original) The apparatus of Claim 1, wherein the medium is the ambient environment.
16. (Original) The apparatus of Claim 15, wherein the ambient environment is one of air and gas.
17. (Currently Amended) An apparatus comprising:
a light emitting diode comprising a chip having a wavelength converting layer that forms a light emitting surface, wherein the light emitting surface is not covered by an encapsulant such that the light emitting surface emits light directly into the ambient environment; and
a collimating optical element disposed to receive the light emitted from the light emitting surface of the chip through the ambient environment.
18. (Original) The apparatus of Claim 17, wherein the collimating optical element is at least one lens.
19. (Original) The apparatus of Claim 17, further comprising a micro-display disposed to receive the light emitted from the light emitting surface of the chip after the light passes through the collimating optical element.
20. (Original) The apparatus of Claim 19, further comprising a secondary collimating optical element disposed between the micro-display and the collimating optical element.
21. (Original) The apparatus of Claim 17, wherein the light emitting diode further comprises a submount, the apparatus further comprising:

a holding element that holds the collimating optical element, the holding element being mounted on the submount.

22. (Original) The apparatus of Claim 21, wherein the holding element has a ring shape and includes a notch and the lens has a tab that is held in the notch.

23. (Original) The apparatus of Claim 17, further comprising:
an array of light emitting diodes, each light emitting diode comprising a chip having a light emitting surface, wherein the light emitting surface is not covered by an encapsulant such that the light emitting surface emits light directly into the ambient environment; and
at least one collimating optical element being disposed to receive the light emitted from the light emitting surfaces of each chip.

24. (Original) The apparatus of Claim 23, wherein the at least one collimating optical element comprises an array of collimating optical elements.

25. (Original) The apparatus of Claim 24, wherein the array of collimating optical elements is an integral array of lenses.

26. (Original) The apparatus of Claim 24, wherein at least one chip is displaced laterally with respect to the center of the associated collimating optical element.

27. (Original) The apparatus of Claim 17, wherein the light emitting diode further comprises a submount and an array of chips mounted on the submount, each chip in the array of chips having a light emitting surface and wherein the light emitting surface of each chip in the array of chips is not covered by an encapsulant, and wherein the collimating optical element is disposed to receive the light emitted from the light emitting surface of each chip in the array of chips through the ambient environment.

28. (Original) The apparatus of Claim 17, wherein the chip includes a wavelength converting layer that forms the light emitting surface.

29. (Original) The apparatus of Claim 17, wherein the ambient environment is one of air and gas.

30. (Original) The apparatus of Claim 17, wherein the chip includes one of a wavelength converting layer, a diffractive layer, a micro-refractive layer, and a filter layer and a polarizer layer that forms the light emitting surface.

31. (Currently Amended) An apparatus comprising:

a light emitting diode comprising a chip having a wavelength converting layer that forms a light emitting surface that emits light having a range of wavelengths into a medium with a refractive index of less than 1.25;

a collimating optical element disposed to receive the light having only the range of wavelengths emitted from the light emitting surface of the chip; and

a micro-display disposed to receive the light emitted from the light emitting surface of the chip after the light passes through the collimating optical element.

32. (Original) The apparatus of Claim 31, wherein the collimating optical element is at least one lens.

33. (Original) The apparatus of Claim 31, further comprising a secondary collimating optical element disposed between the micro-display and the collimating optical element.

34. (Original) The apparatus of Claim 31, wherein the light emitting diode further comprises a submount, and the apparatus further comprising:

a holding element that holds the collimating optical element, the holding element being mounted on the submount.

35. (Original) The apparatus of Claim 34, wherein the holding element is annular and includes a notch and wherein the collimating optical element has a tab that is held in the notch.

Claims 36-42 (Cancelled)

43. (Previously Presented) The apparatus of Claim 1, wherein there are no intervening optical elements between the light emitting surface of the chip and the collimating optical element.

SILICON VALLEY
PATENT GROUP LLP

18805 Cox Avenue
Suite 220
Saratoga, CA 95070
(408) 378-7777
FAX (408) 378-7770